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Seventh Semester B.E. Degree Examination, Dec.2016/Jan.2017 Object Oriented Modeling and Design										
Ti	me: 3	hrs. Max. Max. Max. Max. Max. Max. Max. Max	arks:100							
1	a. b.	PART – A What is OO development? Explain its methodology and its themes. What are Models? Discuss their classification. What is Generalization? Illustrate and discuss with an example.	(10 Marks) (05 Marks) (05 Marks)							
2	а. b.	List and explain various restructing techniques used with respect to workarounds. What is multiple inheritances? Explain different kinds of its with an example illus	(10 Marks) tration. (10 Marks)							
3	a. b.	What is Aggregation concurrent? Illustrate with an example. Discuss use case relationship and draw the use case diagram of stock brokerage sy	(04 Marks) ystem. (08 Marks)							
4	с. а.	With neat diagram. Explain the process overview.	(08 Marks) (06 Marks)							
	b. с.	Explain the steps performed in constructing a domain state model with an example Explain the class model for an ATM system.	(08 Marks) (06 Marks)							
5	a. b.	 For an ATM example i) Analyze the difference type use cases by designing a use case diagram. ii) Explain process transaction scenario. What is system design? Explain steps involved in it. 	(12 Marks) (08 Marks)							
6	6 a. b. c.	List and explain the steps involved in design of algorithms. Write a brief note on Realizing algorithm. Write a note on wrapping.	(08 Marks) (08 Marks) (04 Marks)							
	7 a. b.	With an illustration, explain the dynamics of client-dispatcher-server design patterns.	ern. (10 Marks) (10 Marks)							
1	8 a. b.	Write short note on Unified modeling Language View handler pattern Command processor pattern								
	d.	. Idioms and styles.	(20 Marks)							

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Important Note : 1. On completing your answers, compulsorily draw diagonal cross lines on the remaining blank pages. 2. Any revealing of identification, appeal to evaluator and /or equations written eg, 42+8 = 50, will be treated as malpractice.



Seventh Semester B.E. Degree Examination, Dec.2016/Jan.2017 Embedded Computing System

Time: 3 hrs.

1.2

Max. Marks:100

Note: Answer FIVE full questions, selecting at least TWO questions from each part.

PART - A

	i	a.	What is an embedded system? Differentiate between a general purpose comput	er and an
lice	-		embedded system.	(04 Marks)
4		b.	Explain the design of a requirement form for the beginning of project.	(08 Marks)
1		c.	Discuss various challenges in embedded computing system design.	(08 Marks)
	5 - 10 - F		ਮ ਦੇ ਦਿਸ਼ਿਸ਼ ਹੈ। ਕਿ ਕਿਸ ਕਿਹਾ ਕਿ ਕਿ ਕਿਸ ਕਿਹਾ ਕਿ ਕਿ ਕਿਸ ਕਿਹਾ ਕਿ ਕ	
e.	2	3.	Differentiate between:	
			i) Cache hit and cache miss with a neat diagram.	
20			ii) LDRH and LDRB of ARM instructions.	(04 Marks)
ank s 0, w ii		b.	What is an interrupt? Explain the mechanism of interrupt vectors with a neat diagr	am. (08 Marks)
10 %		C.	Write ARM assembly code to implement the following C statement:	
2+5 2+5			Z = (a << 2) (b & 15).	(04 Marks)
rema eg.4		d.	What is pipeline? Explain the stages in an ARM pipeline.	(04 Marks)
the			Function the following terms: i) Bus master: ii) Four-cycle handshake	protocol;
0.3	3	а.	Explain the following terms. I) Dus master, in Four opere management	(12 Marks)
ine ons		1	Explain how bridge can be used to connect different speeds systems.	(08 Marks)
ross l		b.	Explain, now bridge can be used to connect unrelent speeds systems.	(,
5 J.	4	8	Explain three techniques used in loop optimization.	(10 Marks)
nog		b.	Explain the role of assembles and links in the compilation process with a neat diag	gram.
dia r a		0.		(07 Marks)
drav value		c.	What is dead code elimination? Explain. $\mathbf{PART} - \mathbf{B}$	(03 Marks)
)	-	a	What is a process? What is Kernel? Explain any three services of the Kernel in a	n operating
ulso	~	, u.	system.	(08 Marks)
ap		h.	Compare thread v/s process.	(07 Marke)
8, C		C.	Define the following terms: i) CPU utilization; ii) Throughput; iii) Turnar	ound time;
wet 230			iv) Waiting time; v) Response time.	(05 Marks)
Itil				
/01 F	(6 a.	Explain the working of a shared memory communication system with a neat diag	ram.
g of			i i i i i i fait de la complete quetam perform	(08 Marks)
a		Ь.	List various assumptions done during the evaluation of operating system perform	(04 Marks)
luc			E 1 - (1 - C. Ileming with respect to IPC; i) signals; ii) mail-boxes	(08 Marks)
On c		c.	Explain the following with respect to fr C. 1) signals, hy man coxes.	(,
		7 0	With a neat diagram explain CAN data frame format.	(10 Marks)
	ц і – т	h a.	Explain the basic format of an IP nacket.	(10 Marks)
ICN.		U.	Explain the basic format of an in packets,	
tant		8	Explain the following file types generated during cross-compiling/assembling:	
10.1	-	0	i) obJ File	
In			ii) Map File	
			iii) . Hex File	
			iv) . lst File.	(20 Marks)
			* * * *	



Seventh Semester B.E. Degree Examination, Dec.2016/Jan.2017 Programming the Web

Time: 3 hrs.

Max. Marks:100

Note: Answer any FIVE full questions, selecting atleast TWO questions from each part.

PART – A

1	a. b. c.	Explain URL, MIME with proper examples. Explain HTTP request phase/header. Explain the following tags :	(06 Marks) (08 Marks)
		, , <pre>, <block quote="">, , <a>.</block></pre>	(06 Marks)
2	a. b. c.	Write a program to illustrate the nested ordered list in XHTML.Explain the selector forms with an example.Write a XHTML program to illustrate a form which accepts buyer's Name, AdsState, zip, Product name (Book, Mobile, Pen drive), Price, Quantity, Payment memaster card, Discover, Check), submit button and clear form button.	(07 Marks) (06 Marks) dress, City, thod (Visa, (07 Marks)
3	a.	Explain the JavaScript screen output and keyboard input functions with neat diagr	ams.
	h	Explain object creation and modification in JavaScript	(06 Marks)
	с.	Write a JavaScript program to check the validity of phone number.	(08Marks)
4	а	Explain the document object model	(06 Marks)
	b.	Illustrate with JavaScript program handling of events from button element.	(06 Marks)
	c.	Explain positioning of elements.	(08Marks)
		PART – B	
5	a.	Explain definition of XML name spaces with examples.	(07 Marks)
	b.	List and write significance of XML datatypes.	(07 Marks)
	c.	Write a note on XML processors.	(06 Marks)
6	a.	Explain arrays in Perl with examples.	(08 Marks)
	b.	Explain pattern matching in Perl.	(06 Marks)
	C.	Explain CGI linkage in client-server application.	(06 Marks)
7	a.	Explain the different control statements used in PhP with a PhP program.	(07 Marks)
	b.	Explain session tracking in web applications.	(06 Marks)
	C.	Write a note on PhP files.	(07 Marks)
8	a.	Write a note on methods in Ruby.	(07 Marks)
	b.	Write a note on dynamic documents in Rails.	(06 Marks)
	с.	Explain the layouts with respect to rails.	(07 Marks)



Seventh Semester B.E. Degree Examination, Dec.2016/Jan.2017 Advanced Computer Architecture

Time: 3 hrs.

1

Max. Marks:100

Note: Answer FIVE full questions, selecting at least TWO questions from each part.

PART – A

- a. Define the term-instruction set architecture. In what way computer architecture is related to ISA? Correlate them. (04 Marks)
 - b. Elaborate the different parameters that decide the cost of an IC. Give the equation of each parameter separately and explain them. (06 Marks)
- c. Define the term-CPI and derive the equation for finding the total number of processor cycles needed to execute a program. Consider the execution of an object code with 200,000 instructions on a 20MHz processor. The program consists of four major types of instructions. The instruction mix and the number of cycles (CPI) needed for each instruction is given below based on the result of a program trace experiment.

Sl. No.	Instruction type	C.P.I.	Instruction mix
1	Arithmetic and logic	1	68%
2	Load/store with cache hit	2	8%
3	Branch	4	14%
4	Memory reference with cache hit	8	10%

- i) Find the total number of cycles required to execute the program.
- ii) Calculate the average C.P.I. when the program is executed on a uniprocessor system with the above trace results.
- iii) Calculate the corresponding MIPS rate based on the CPI obtained in (i) above.

(10 Marks)

- a. Discuss the various kinds of data dependencies that can cause problems to the smooth flow of instructions through pipelines. Give supporting example in each case and explain with an example how these dependencies can be eliminated. (10 Marks)
 - b. Explain the principles of loop unrolling. Demonstrate the normal loop execution and loop unrolling concepts for the following C-code segment by translating the given code segment given below, to MIPS assembly language code.
 - C code: for (i = 1000; i > 0; i = i 1) X[i] = X[i] + s where s = scalar value.
 - i) Calculate the number of clock cycles required per element for both unscheduled and scheduled loops in normal case considering stalls/idle clock cycles.
 - ii) Repeat the above step for loop unrolled execution case with and without schedule.
 - iii) Calculate the average value of clock cycles per element for the (i) and (ii).

(10 Marks)

- a. With reference to Branch Target Buffers (BTBs) explain.
 - i) The purpose of each B.T.B. entry and
 - ii) The meaning of the following terms and the subsequent action taken for each of the following occurrence of events.

Case(1): Branch entry found in BTB entry and predicted branch not taken. Case(2): Branch entry found in BTB entry and predicted branch not taken. (08 Marks)

b. Name the different techniques used for getting high performance of pipelines with multiple delivery techniques. What are integrated instruction Fetch units (IIFU)? Highlight on the basic function of such fetch units.
 (08 Marks)

c. Compare register Re-naming technique with that of Re-order buffer technique in speculation concept.
 (04 Marks)

1 of 2

2

3

- 4 a. With appropriate timing diagrams, explain the concept of delayed branch technique used in RISC processors. What are its limitations and demonstrate the scheduling of branch delay with suitable examples. (10 Marks)
 - b. Explain what is branch penalty? Discuss the different techniques used to reduce branch penalties. (06 Marks)
 - c. Consider a non-pipelined processor in RISC. Assume that it has 1ns clock cycle and that it uses 4 cycles for ALU operations and branches and 5 cycles for memory operations. Assume that the relative frequencies of these operations are 40%, 20% and 40% respectively. Suppose that due to clock skew and set up, pipelining the processor adds 0.2ns of overhead to the clock. Ignoring any latency impact, how much speed up in the instruction execution rate is achieved from this pipeline? (04 Marks)

PART – B

- a. Explain with the help of appropriate pseudo statements the principle of spinlocks with 5 EXCH synchronization primitive, highlight on its demerits. Explain the modified spinlock primitive psuedocode and its merits. (10 Marks)
 - b. Explain the meaning of the following terms used in cache controlled state transition diagram: i) Exclusive; ii) Shared and iii) invalid. Draw the state transition diagram for:

- Processor (C.P.U.) requests for each cache block. i)
- Bus requests for each cache block and list all the responds to the events for (i) and (ii) ii) in tabular form. (10 Marks)
- a. Explain the different compiler optimization techniques used to reduce miss rate. 6 (10 Marks)
 - b. Explain the process of:
 - i) Protection via virtual memory.
 - ii) Protection via virtual machines.
- a. Explain any four memory hierarchy questions in detail. 7 (08 Marks)
 - b. Explain the different techniques used to improve memory performance inside a DRAM chip. (08 Marks)
 - c. A parallel processing system -C is having a degree of parallelism = 10. If f = fraction of the operations performed by C and are strictly scalar (cannot be processed in parallel), speed up for the tasks under consideration = 6.5, assuming that all other operations are processed at the maximum possible rate (vector),
 - What is f? i)

ii) By how much f be reduced to increase the speed to 9.0?

- Write short notes on any Four: 8
 - a. Benchmarks.
 - b. Detecting and enhancing loop level parallelism.
 - c. Hardware support for compiler speculation.
 - d. Memory consistency models.
 - e. What makes pipelining hard to implement?

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2 of 2

(10 Marks)

(04 Marks)

(20 Marks)

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10IS72

Seventh Semester B.E. Degree Examination, Dec.2016/Jan.2017 Information Systems

Time: 3 hrs.

Max. Marks:100

Note: Answer FIVE full questions, selecting at least TWO questions from each part.

PART – A

1	a. b. c.	What is fundamental role of information systems in business? What is meant by cybernetic system? Explain. Elaborate the types of information system, with neat diagram.	(05 Marks) (05 Marks) (10 Marks)
2	a. b.	List and describe the five basic competitive strategies, with a neat diagram. How customer focused business builds a customer value and loyalty usin technologies? Explain.	(10 Marks) g internet (10 Marks)
3	a. b. c.	Illustrate with a neat diagram, the different stages of a transaction processing explain them. Briefly explain the component of target marketing. State the objectives of computer integrated manufacturing.	cycle and (08 Marks) (08 Marks) (04 Marks)
4	a. b. c.	What is SCM? Explain benefits and challenges of SCM. What is CRM? Explain different phases of CRM. What is ERP? Explain benefits and challenges of ERP and the costs of ERP.	(06 Marks) (04 Marks) (10 Marks)
		PART – B	
5	a. b.	Explain essential E-Commerce process and process architecture. State and explain some of the key factors for success in E-Commerce.	(12 Marks) (08 Marks)
6	a.	What is DSS? With a neat block diagram, explain the components of web enabled DSS	marketing
	b. c.	Explain on line analytical processing using DSS. Explain web-based knowledge management system.	(05 Marks) (05 Marks)
7	a. b.	What is Hacking? Explain the common hacking tactics to assault the companies. Explain: i) Encryption with figure; ii) Firewalls with figure.	(10 Marks) (10 Marks)
8	a.	Explain the major components of business/IT planning process and IT architecture	ð.
	b.	Describe the top issues in managing international data communication.	(10 Marks) (10 Marks)

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	Seventh Semester B.E. Degree Examination, Dec.2016/Jan.2017												17	
	Data Warehousing and Data Mining													
Time: 3 hrs. Max. Marks													arks:100	
No	ote:	Answer	any F	IVE	ful	l que	estion	ns, sele	cting a	tleast 7	TWO que	estions fro	m e	ach part.
1	a. b. c.	Give the general List the What is	e defini Data w major : Meta c	tion areho steps lata?	of D ousii invo Diso	ata w ng aro olved cuss o	vareh chiteo in E liffer	lousing. cture. TL proc ent type	PART – With a cess. es of me	<u>- A</u> schema ta data -	tic diagra used in Da	m, explain ata wareho	the vuse.	working of (08 Marks) (08 Marks) (04 Marks)
2	a. b.	Explain Describe	Codd' e data o	s OL cube	AP oper	chara ration	cteris s, wi	stics. th an ex	ample.	C	X			(10 Marks) (10 Marks)
3	 a. Discuss the challenges that motivate the development of Data mining. b. Consider the following vectors. Find i) Simple matching co-efficient ii) coefficient iii) Hamming distance. X = 0101010001 Y = 0100011000. 											(06 Marks) Jaccard (04 Marks)		
	С.	Describ	e any f	ive d	ata p	orepro	ocess	ing app	roaches					(10 Marks)
4	a. b. c.	What is Develop Explain	Assoc the A the va	iatior prior rious	n An i alg mea	alysi orith asure:	s? De m foi s of e	efine Su r freque evaluation	nt items	nd confi et gener ciation p	dence, wir ration, wit atterns.	th an exam h an exam	ple.	(04 Marks) (08 Marks) (08 Marks)
5	a. b. c.	Explain Give the Conside followir i) Rule R1 : A - R2 : B - R3 : C	the va e differ er a traing e accure \rightarrow + (c \rightarrow + (c \rightarrow + (c	rious rence ining didat acy overs overs	me: bet set ii s 4 + s 30 s 100	asure: ween that c le. D) Fo - ve a +ve a 0 +ve	s for rule contai eterm il Inf nd 1 and 1 and	selectin based o in 100 - nine wh formatic - ve ex 0 -ve ex 90 -ve	PART - ig the be rdering +ve exan hich is on gain. amples) xamples example	<u>- B</u> est split and class mples an the best () () ()	with an ex ss based o nd 400 –v t and wor	ample. rdering sch e example: rst candida	neme s for ite ac	(06 Marks) . (04 Marks) each of the cording to (10 Marks)
6	a. b.	Explain Explain	Bayes how t	ian c he pr	lass	ificat tive a	ion. Iccura	acy of c	lassifica	ation me	ethods be	estimated.		(10 Marks) (10 Marks)
7	a. b. c.	Explain Explain Write s	i desire i how c hort no	d fea listan ote on	iture ice b i der	s of c betweensity l	luste en a j pased	er analys pair of p I metho	sis. points ca ds.	an be co	mputed.			(10 Marks) (05 Marks) (05 Marks)
8	a. b.	Explain web. Write s	n web c hort nc	conte otes o	nt m on :	nining i) Sp	g and atial	how it data mi	is used ining	for disc ii) Te	overing u ext mining	seful infor	matic	on from the (10 Marks) (10 Marks)

Important Note : 1. On completing your answers, compulsorily draw diagonal cross lines on the remaining blank pages. 2. Any revealing of identification, appeal to evaluator and /or equations written eg, 42+8 = 50, will be treated as malpractice.

Seventh Semester B.E. Degree Examination, Dec.2016/Jan.2017 JAVA and J2EE

Time: 3 hrs.

USN

Max. Marks:100

Note: Answer FIVE full questions, selecting at least TWO questions from each part.

$\underline{PART - A}$

1	a. b. c. d.	List and explain JAVA buzzwords. Explain how arrays are defined and used in JAVA with an example. List and explain access specifiers in java and their scope. Explain : i) >>> ii) foreach.	(05 Marks) (05 Marks) (06 Marks) (04 Marks)
2	a. b. c.	What are command line arguments? While a Java program to read a number as line argument and check whether it is prime or not. What is an exception? Explain the usage of throw keyword with an example. What is an applet? Write an applet program to display the message "A SIMPLE	command (04 Marks) (06 Marks) APPLET". (06 Marks)
3	а. b. c.	What is thread? Explain two ways of creating a thread in JAVA with example. What is the need of synchronization? Explain with an example how synchro implemented in JAVA. What is delegation event model?	(04 Marks) (10 Marks) nization is (06 Marks) (04 Marks)
4	a. b.	Describe the different types of swing buttons. Write the steps to create J-table. Write a program to create a table with colum "Name, USN and age". Insert at least 5 records into the table and display.	(10 Marks) an heading (10 Marks)
5	a. b. c.	PART – B Describe the various steps of JDBC with code snippets. What are transactions? Write a Java program to execute database transaction. Explain: i) Prepared statement ii) Callable statement.	(08 Marks) (08 Marks) (04 Marks)
6	a. b. c.	Explain the servlet life cycle with example Define a cookie. Write a servlet program to add a cookie. Write a program to describe parameter reading using servlets.	(06 Marks) (07 Marks) (07 Marks)
7	a. b.	Define JSP. Explain different types of JSP tags. What is RMI? Explain the server side and client side methods.	(10 Marks) (10 Marks)
8	a. b. c. d.	Write short notes on : EJB transaction attributes Session java bean Stateless and stateful session bean Message driven bean.	(05 Marks) (05 Marks) (05 Marks) (05 Marks)

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10CS/IS761

Seventh Semester B.E. Degree Examination, Dec.2016/Jan.2017 C# Programming and •NET

Time: 3 hrs.

Max. Marks:100

Note: Answer any FIVE full questions, selecting atleast TWO questions from each part.

PART – A

1	a.	Define •NET. Explain the limitation and complexities found within the technologies prior to
	b.	With the help of neat diagram describe the workflow that takes place between your source code, a given NET compiler and the NET execution engine
	c.	CLS rules apply only to those parts of a type that are expressed outside the defining assembly. Justify with illustration. (06 Marks)
2	a.	Define metadata and manifest. Give the procedure to explore the metadata, manifest and
	b.	Describe the meaning of the following C# compiler options:
	c.	1) (2) 11) /? 111) /addmodule 1V)/baseaddress v) /bugreport vi) /checked vii) /codepage viii) /doc. (08 Marks) Explain the following aspects of VS•NET (IDE) :
		i) Solution explorer ii) Documenting code Via XML. (06 Marks)
3	a.	Write a C# program that accepts five integers as a command line arguments and prints them in ascending order. The main method definition should have no arguments. (04 Marks) Explain the following $C^{\#}$ programming consists with an example i
	0.	i) foreach/in ii) ref iii) params iv) enum. (08 Marks)
	C.	List out the different members of System Object and explain how would you override the
		i) Tostring ii) Equals iii) GetHashCode(). (08 Marks)
4	a.	With illustration explain the following C# static types : i) static methods ii) static data iii) static class
	b.	iv) static constructor v) static properties. (10 Marks) What is containment/delegation? Illustrate with appropriate programming snippets
	C.	Differentiate between eleccical and adhee netween him with even al (06 Marks)
		Differentiate between classical and autoc polymorphism with example. (04 Marks)
		PART – B
5	a.	Discuss with illustration how the encapsulation can be enforced using traditional accessors, mutators and class properties. (08 Marks)
	b.	Define errors, bugs and exceptions. Discuss what is the role of •NET exception handling.
	C.	Write a C# application to illustrate, how to handle multiple exceptions. (06 Marks) (06 Marks)
6	a.	What is meant by object lifetime? Describe the role of •NET garbage collection, finalization
	b	process and adhoc destruction method with examples. (10 Marks)
	с.	Write a C# program to demonstrate a "Generation" objects.(05 Marks)

7	a.	Differentiate between the following : i) Abstract classes and interfaces
		ii) Synchronous and asynchronous delegates. (08 Marks)
	b.	With an illustration discuss how the interface can be used to implement multiple inheritance.
	c.	Write a program in C# to simulate simple calculator using delegates.(06 Marks)(06 Marks)
8	a. b.	Mention the major elements of •NET binaries and explain each component.(10 Marks)Explain the steps involved in building and consuming a multiple assembly.(10 Marks)



Seventh Semester B.E. Degree Examination, Dec.2016/Jan.2017 **Artificial Intelligence**

Time: 3 hrs.

1

Max. Marks:100

Note: Answer any FIVE full questions, selecting atleast TWO questions from each part.

PART – A

1	a.	Explain the properties of task environments with example.	(10 Marks)
	b.	Explain the structure of a model based reflex agent with a diagram.	(04 Marks)
	c.	Briefly explain the depth limited search and iterative deepening depth first search.	A STATUTE AND A DESCRIPTION OF A DESCRIPTION
			(06 Marks)
2	a.	State and prove the conditions that heuristic function h(n) should satisfy so that A	search is
		both complete and optimal.	(10 Marks)
	b.	Explain how backtracking search is used in solving constraint satisfaction proble	em with an
		example.	(08 Marks)
	с.	Define a game as a kind of search problem.	(02 Marks)
			a 6
3	a.	Give the PEAS description of Wumpus world.	(05 Marks)
	b.	Illustrate the procedure of converting the following sentence into conjunctive norm	nal form.
		$B_{1,1} \Leftrightarrow (P_{1,2} \lor P_{2,1})$	(06 Marks)
	C.	Write BNF grammar of sentences in propositional logic.	(06 Marks)
	d.	Define equivalence, validity and satisfiability in logic.	(03 Marks)

Explain the knowledge engineering process for the domain of electronic circuits. 4 a. [Refer Fig.Q4(a)] (10 Marks)



Fig.Q4(a)

Explain the syntax of first order logic with equality, specified in BNF. Give example for b. each word. (10 Marks)

PART - B

The law says that it is a crime for an American to sell weapons to hostile nations. The 5 a. country Nano, an enemy of America, has some missiles and all of its missiles were sold to it by west, who is American. Prove that west is a criminal using resolution. (12 Marks)

b. Determine the color of Fritz from the following knowledge base using (i) forward chaining (ii) backward chaining.
If x croaks and eats flies, then x is a frog. If x chirps and sings, then x is a canary. If x is a frog, then it is colored green if x is canary, then x is colored yellow. Fritz croaks and eats

6 a. Explain how categories and objects are represented and defined with suitable examples.

flies.

8

(06 Marks)

(08 Marks)

- b. What is situation calculus? Explain the ontology of situation calculus. (06 Marks)
- c. Write short notes on : (i) Semantic networks (ii) Truth maintenance systems. (08 Marks)
- 7 a. The full joint distribution for three variables Toothache, cavity and catch are given below.

	-		Тоо	thache	¬ То	othache		
			Catch	- Catch	Catch	- Catch		
		Cavity	0.108	0.012	0.072	0.008		
		- Cavity	0.016	0.064	0.144	0.576		
	Find (i) P (cavity to	oothache)						
	(ii) P (toothache	$e \wedge catch c$	avity)					
	(iii) P (cavity)					•		(06 Marks)
b.	What is a Bayesian ne	twork? Exp	lain the	semantics	of Baye	sian netwo	ork with an	example.
	with the total states of total states	c · · · 1	~					(10 Marks)
С.	Write ADL description	n of simple	spare fu	re problem				(04 Marks)
a.	Write the decision tree	e learning a	lgorithm	with an e	xample.			(10 Marks)
b.	Explain the issues affe	ecting desig	n of a le	arning eler	ment.			(10 Marks)

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10CS/IS765

Seventh Semester B.E. Degree Examination, Dec.2016/Jan.2017 Storage Area Networks

Time: 3 hrs.

Max. Marks:100

Note: Answer FIVE full questions, selecting at least TWO questions from each part.

PART – A

1	a. b. c.	Discuss the key characteristics of a Data Centre, with a neat diagram. What is information life cycle management? Describe ILM implementation in detabenefits. Consider a disk I/O system in which an I/O request arrives at the rate of 80 IOPS service time is 6 ms. Compute (i) Utilization of I/O controller (ii) Total respon	(06 Marks) ail, with its (10 Marks) S. The disk se time. (04 Marks)
2	a.	Describe RAID levels with reference to nested RAID, RAID 3, RAID 5 with near	at diagram. (10 Marks)
	b.	With the neat diagram, explain the structure of read and write operations in cache.	(10 Marks)
3	a. b.	Explain connectivity options of FC architecture with relevant diagram. Explain the components of ISS with reference to LUN and LUN masking with dia	(10 Marks) gram.
	c.	Explain FC frame.	(06 Marks) (04 Marks)
4	a. b.	Describe NAS implementation and benefits with neat diagram. What is iSCSI? Explain protocol stack with neat diagram.	(10 Marks) (10 Marks)
5	a. b.	<u>PART – B</u> Explain object storage and retrieval in CAS with suitable diagrams. Discuss storage virtualization types in detail and also discuss its challenges.	(10 Marks) (10 Marks)
6	a. b.	What is information availability? Explain how information availability is de measured. Describe the failure analysis in BC. Mention some important BC technology solut	efined and (08 Marks) ions. (12 Marks)
7	a. b.	Explain Pointer based virtual replication with respect to write to source and write with neat diagram. What is remote replication? Explain different modes of remote replication diagram.	te to target (12 Marks) with neat (08 Marks)
8	a. b.	Write a note on risk traid. Explain in detail. Explain storage management activities in detail, with example.	(10 Marks) (10 Marks)